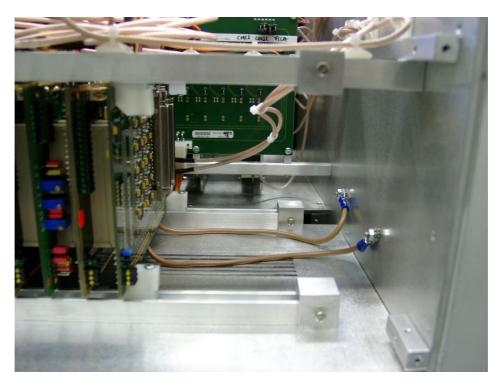
## <u>FilaOut: VSI performance improvement and board checkout</u> <u>M. Wunderlich 21Oct2012-11-21 mwunderlich@mpifr-bonn.mpg.de</u>

For some reasons the shielding of a VSI cable was not connected to the DBBC ground in earlier systems. This could produce common mode biases in the drivers and thus a strange behaviour of the VSI in some cases.

It is quite easy to correct this. Use 4 flathead screws M2.5 to fasten the VSI connectors to FilaOut board properly. Use two of these screws to connect ground straps to the rectangular copper area which is floating in respect to the system ground.



Connect these ground straps to the DBBC housing. Ensure a good contact. In our example the side wall was used. In case of an internal Fila10G this space is occupied. Any other position will do.



## **R6** problem:

There have been a couple of problems due to a manufacturing error. On some of the boards the resistors shown below had not been populated. In some cases this prevents the VSI output from working properly (intermittant signals and that sort of things). Make sure that these resistors have been placed, the most crucial one is R6. All three of them are standard SMD components size 0603. The nominal value is 10KOhm, but if these are not available at your site, any value between 1K and 10K will do.

